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IN THE UNITED STATES PATENT AND TRADEMARK OFFICEPATENT

Application No.: 10/028,880) Date: November 28, 2003
)
Applicant: Ta-Ko Chuang, et al.) Examiner: Todd J. Kilkenny
) Group Art Unit 1733
Filed: 12/20/2001)
) Our Ref: 619416-6 B-4442
For: "Method for Bonding An Integrated)
Circuit Device..."

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed in response to the Office Action dated October 10, 2003. Entry of the amendments below is respectfully requested pursuant to 37 CFR 1.116(a) as putting the application into condition for allowance. Please amend the claims as indicated below. **All amendments and remarks herein are made without prejudice.**

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.